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That which is claimed follows:

1. A motor system comprising:
a motor; and
a detector which monitors a motor command voltage spectrum for the presence of at least one stall indicating harmonic.
2. The system according to claim 1, wherein the at least one stall indicating harmonic includes an even harmonic.
3. The system according claim 1, wherein the at least one stall indicating harmonic includes a second harmonic.
4. The system according to claim 3, wherein a stall condition is indicated when the amplitude of the second harmonic exceeds a predetermined level.
5. The system according to claim 1, wherein the detector includes at least one filter.
6. The system according to claim 5, wherein the at least one filter includes at least one notch filter or bandpass filter.
7. The system according to claim 1, wherein the motor is a hybrid step motor having 2, 3, or 5 phases.
8. The system according to claim 1, wherein the motor is a variable reluctance motor.
9. The system according to claim 1, wherein the detector is one of a digital detector, an analog detector, and a hybrid detector.
10. The system according to claim 1, wherein the system is an open loop system.

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11. The system according to claim 1, wherein the detector monitors the motor voltage command spectrum for the presence of no more than one stall indicating harmonic.

12. The system according to claim 1, wherein the detector continuously monitors the motor command voltage spectrum.

13. The system according to claim 1, wherein at least two-phase winding voltages are summed by a summing unit.

14. A stall detector for an open loop motor system, said stall detector comprising:
a motor driver for supplying a command voltage signal to a motor; and
a stall detector monitor which monitors the command voltage signal and detects a stall condition as a function of the presence of at least one even harmonic component of the command voltage signal supplied to the motor.

15. The stall detector according to claim 14, wherein the stall detector monitor continuously monitors the command voltage signal.

16. A method of stall detection in a motor comprising:
monitoring at least one stall indicating harmonic associated with a motor command voltage spectrum; and
determining whether the motor has stalled as a function of the at least one stall indicating harmonic.

17. The method according to claim 16, wherein the at least one stall indicating harmonic includes the second harmonic.

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18. The method according to claim 16, further comprising continuously monitoring at least one stall indicating harmonic associated with the motor command voltage spectrum.

19. The method according to claim 18, further comprising determining whether the motor has stalled as a function of the presence of one stall indicating harmonic.

20. The method of claim 16, wherein the motor is controlled in accordance with whether a stall condition has been determined.